

Fig. 1
(Prior Art)

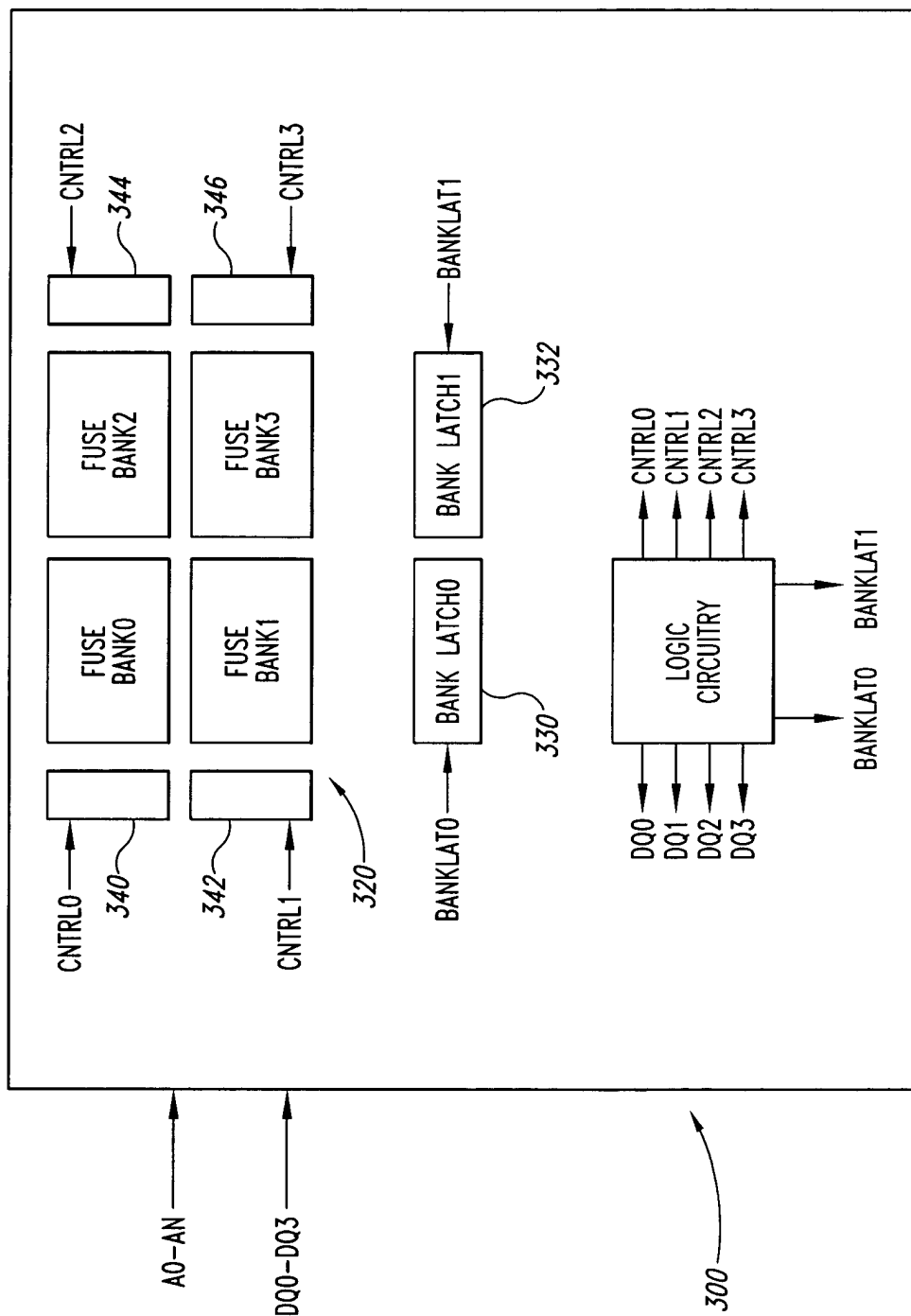


Fig. 2

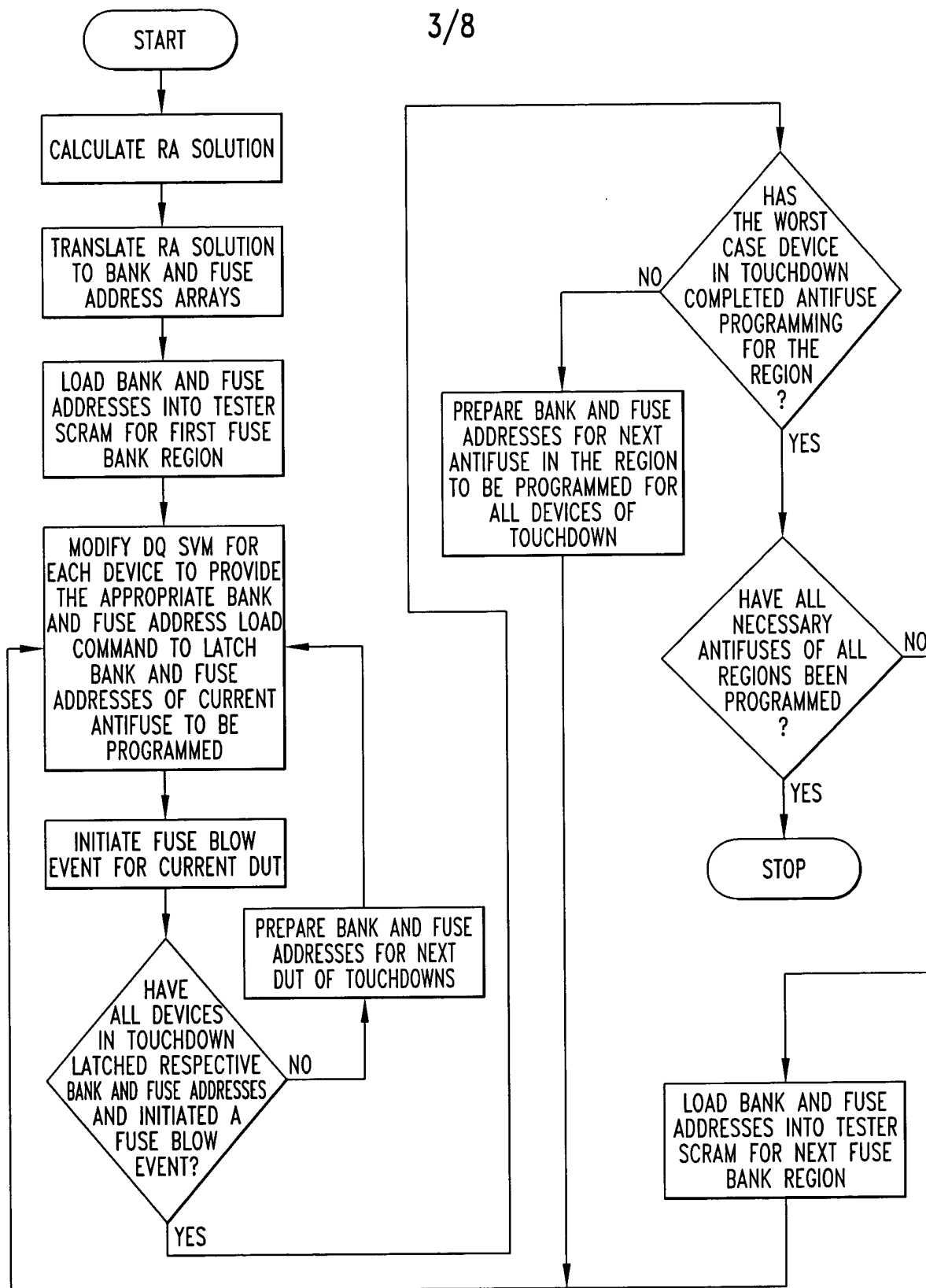
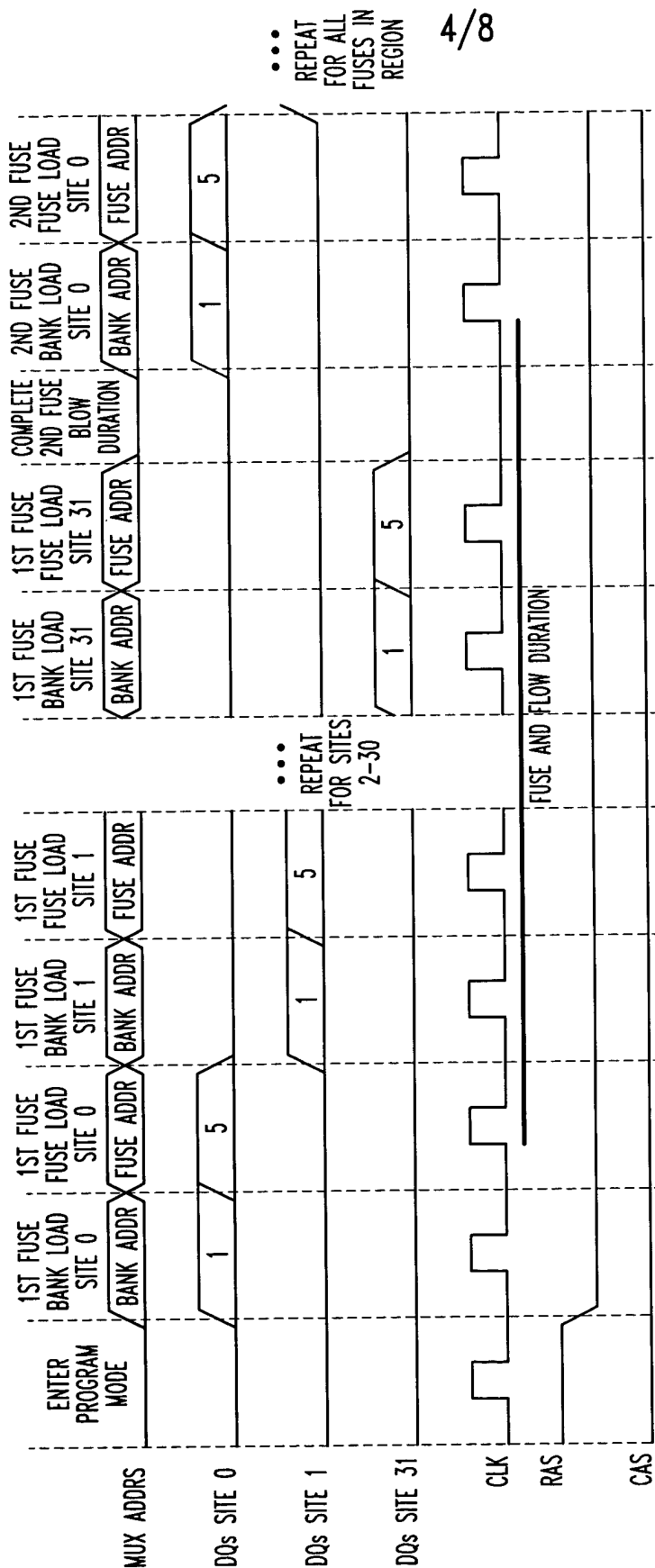


Fig. 3

SIMPLIFIED WAVEFORM FOR J994 FUSE BLOW METHOD

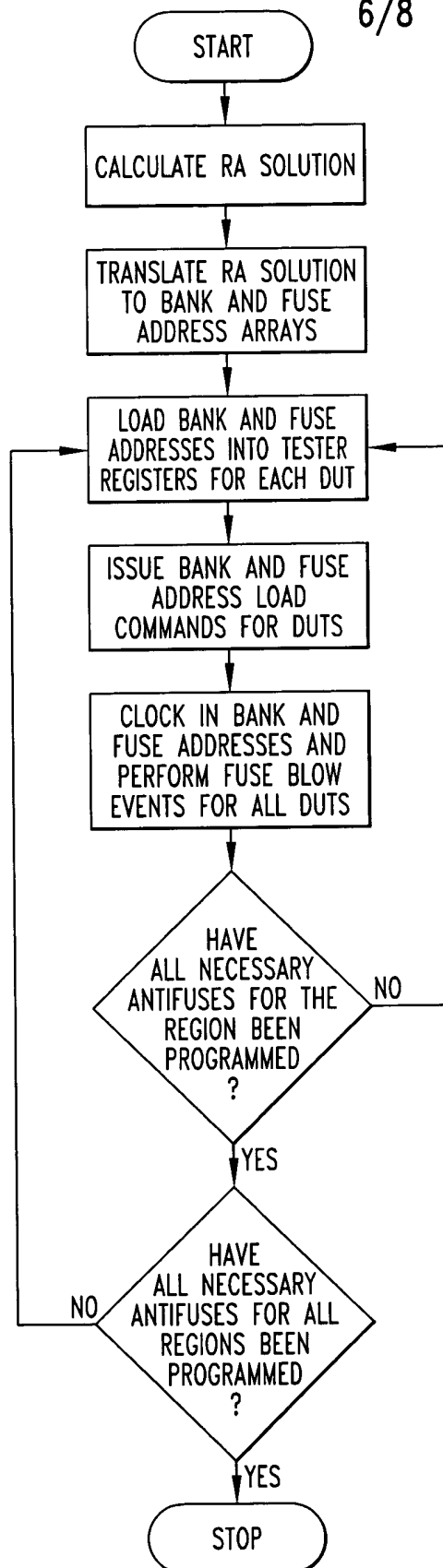


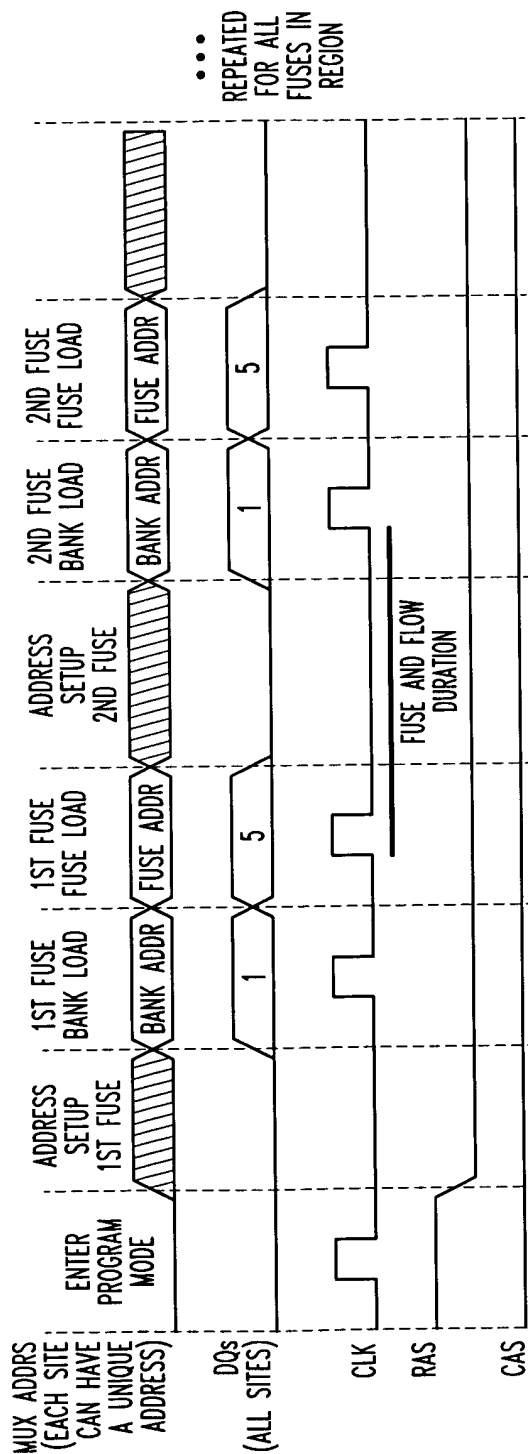
DQs FOR BANK AND FUSE ADDRESS LOADING	FUNCTION
3210	LOAD NOTHING
0000	LOAD BANK ADDRESS LEFT
0001	LOAD BANK ADDRESS RIGHT
0010	LOAD FUSE ADDRESS TOP LEFT
0101	LOAD FUSE ADDRESS TOP RIGHT
0110	LOAD FUSE ADDRESS BOTTOM LEFT
1001	LOAD FUSE ADDRESS BOTTOM RIGHT
1010	LOAD FUSE ADDRESS BOTTOM RIGHT

Fig. 4

SCRAM RAM		DQ SVM DATA					
		DUT 0 0123	DUT 1 0123	...	DUT 31 0123	...	DUT 63 0123
BANK	ADDR	DUT 0	0000	...	0000	...	0000
FUSE	ADDR	DUT 0	0000	...	0000	...	0000
BANK	ADDR	DUT 1	0000	...	0000	...	0000
FUSE	ADDR	DUT 1	0000	...	0000	...	0000
...							
BANK	ADDR	DUT 31	0000	...	0000	...	0000
FUSE	ADDR	DUT 31	0000	...	0000	...	0000
...							
BANK	ADDR	DUT 63	0000	...	0000	...	0000
FUSE	ADDR	DUT 63	0000	...	0000	...	0000

Fig. 5

*Fig. 6*



DQs FOR BANK AND FUSE ADDRESS LOADING

HEX	FUNCTION
0000	LOAD NOTHING
0001	LOAD BANK ADDRESS LEFT
0010	LOAD BANK ADDRESS RIGHT
0101	LOAD FUSE ADDRESS TOP LEFT
0110	LOAD FUSE ADDRESS TOP RIGHT
1001	LOAD FUSE ADDRESS BOTTOM LEFT
1010	LOAD FUSE ADDRESS BOTTOM RIGHT

DEVICE LAYOUT

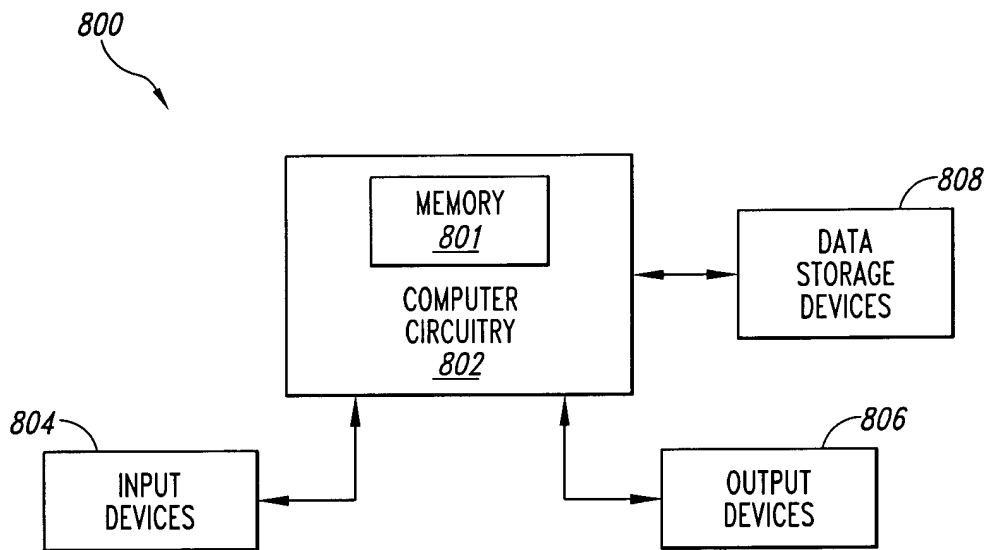
DEVICE BANK (FUSE REGION) 0			
DQs			
3210			
0001	LOAD BANK ADDRESS		
0101	LOAD FUSE ADDRESS		

DEVICE BANK (FUSE REGION) 1			
DQs			
3210			
0001	LOAD BANK ADDRESS		
1001	LOAD FUSE ADDRESS		

DEVICE BANK (FUSE REGION) 2			
DQs			
3210			
0010	LOAD BANK ADDRESS		
0110	LOAD FUSE ADDRESS		

DEVICE BANK (FUSE REGION) 3			
DQs			
3210			
0010	LOAD BANK ADDRESS		
1010	LOAD FUSE ADDRESS		

Fig. 7

*Fig. 8*